Before presenting some of the basic ideas that support the operating and implementing principles of volt–ampere meters, it is worth discussing the concepts of active power, reactive power, and apparent power.

Power is the rate at which energy is transferred or transformed into a different form. Active power *P*, also called true power, is physically related to the energy nonreversibly converted to heat (Joule effect) in the resistive components of electric circuits. Thus, in a resistor *R* with current *i*, *P* is given by

$$
P = RI_{\rm rms}^2 = \frac{U_{\rm rms}^2}{R} = (ui)_{\rm av} = \frac{1}{T} \int_T (ui) \, dt = (p)_{\rm av} \tag{1}
$$

where rms stands for root-mean-square, *u* is the voltage drop in *R*, *p* is the instantaneous power, *T* is the period of *p*, *u* and *i* represent instantaneous valves, and U_{rms} and I_{rms} represent constant valves.

Equation (1) is the basic definition of active power in a circuit. Active power is the time average of the product of instantaneous voltage and current (instantaneous power). When, and only when, voltage and current are sinusoidal, Eq. (1) yields the known relationship

$$
P = U_{\rm rms} I_{\rm rms} \cos \varphi \tag{2}
$$

where φ is the phase angle between *u* and *i*. The unit of active power is the watt (W).

Contrary to the active power which has a physical meaning, reactive power (R) and apparent power (S) are electrical quantities that have the dimensions of power but have significance only when voltage and current are sinusoidal or near sinusoidal. These two quantities are related to the behavior of the reactive components of circuits and their meanings result from interpreting the complex Poynting theorem (1). For a circuit under a sinusoidal regime,

$$
R = U_{\rm rms} I_{\rm rms} \sin \varphi \tag{3}
$$

$$
S = U_{\rm rms} I_{\rm rms} = \sqrt{P^2 + R^2} \tag{4}
$$

Reactive power is expressed in volt–ampere reactive (VAR) and apparent power in volt–ampere (VA).

TYPES OF VOLT-AMPERE METERS

Nowadays, because of the widespread use of personal computers (PCs) and PC data acquisition boards (DAQs), the design of multipurpose measuring instruments that function in particular as volt–ampere meters is a problem of software as well as hardware. And because the implementation of the hardware to digitize several electric voltages and currents is trivial in many situations, the measurement capacity of a PC with a DAQ depends mainly on the software component of such a measuring instrument, usually designated by a virtual instrument (VI). The use of object-oriented languages and the **VOLT–AMPERE METERS** existence of commercial software packages supported and supporting visual programming has made life easier for VI Volt–ampere meters are multipurpose electric measuring in- designers and also for users, because those programming struments that display the values of active, reactive, and ap- tools facilitate the implementation of instruments with userparent electric power. Some of them also measure other elec- friendly interfaces. It is our belief that in the near future PCtric quantities, such as current, voltage, and energy. based virtual instrumentation will render obsolete many ded-

icated analog and digital instruments still in use now, particularly in the laboratory. Volt–ampere meters will be no exception. Model TPZ 303 from ZERA Electric GMBH is an example of this type of instrumentation already available. It is a multifunction, high-accuracy (200 ppm for active power) measuring system based on the digitization of the input quantities and signal processing. Designed to be a portable standalone instrument, the processing unit is not an external PC but an internal digital signal processor (DSP). However, an overview of some of the other types of volt–ampere meters is justified, because instruments based on different operating principles are still manufactured and used, particularly in in-
dustry.
The presentation of volt-ampere meters that follows is or-
 R_a is an additional resistance used in the voltage circuit.
The presentation of volt-amp

ganized according to the power measurement principles they use.

According to the measuring method, power meters may be
divided into analog and digital types. In analog power meters,
the measurement is obtained by an electric quantity that is
proportional to the power. This quantity is the measurement of power is obtained upon adequate reduction of the resulting data. Presentation of the results is usually digital. A *VI* power meter is an example of a digital

tive resistor (bolometer) absorbs energy. The corresponding change of resistance is measured by a bridge circuit; (4) measurement of part of the heat produced in a resistor using the bolometer method (calorimetric active power meters). These the reactive power may be measured if the current in the methods may be used to implement low-frequency active moving coil is not proportional to the load voltage, but to a power meters. Nevertheless, the meters commercially avail- voltage lagging the load voltage by $\pi/2$. Figure 2 shows the able are almost exclusively designed for the radio-frequency voltage circuit of an electrodynamic reactive power meter domain. For that reason, those types of meters are not consid- (electrodynamic varmeter) and the vector diagram of the perered in this article. Interested readers are directed to Refs. 2 tinent electrical quantities. The vector diagram presented is and 3. valid only for one frequency ω , which means that the electro-

tems and so the solutions considered in the following para- surements are correct only when the load current and voltage graphs are used in low-frequency power meters. are sinusoidal at the frequency considered in the design of the

have two coils, one fixed (current coil or current circuit) and be preceded by circuitry to output dc voltages or currents pro-

$$
\alpha = k(i_1 i_2)_{\text{av}} = k \frac{1}{T} \int_T i_1(t) i_2(t) dt
$$
 (5)

power meter.

To measure the active power in a load $Z_{\rm L}$, the two coils are

From Eqs. (1), (3) and (4), one may conclude that any idea

that yields the analog product of two voltages or currents sup-

entitive power

$$
\sin \varphi = \cos \left(\varphi - \frac{\pi}{2}\right) \tag{6}
$$

Reactive and apparent powers are important in power sys- dynamic varmeter is a single-frequency varmeter. The meainstrument. Commercial electrodynamic varmeters for mea-**SURFER SURFER SURF**

The electrodynamic principle also allows constructing an **Electrodynamic Principle (Electrodynamic Volt–Ampere Meters)** instrument for measuring apparent power directly. For that Electrodynamic instruments are analog instruments that purpose, the two coils of an electrodynamic instrument must

volved in the circuit under test. This solution, however, is not and their filaments (heaters) have resistances R_f . Because the used in the practice. circuit is symmetrical, the filament currents of the converters

The series connection of the two coils of an electrodynamic i_A and i_B are given by instrument transforms it into a true rms current meter (electrodynamic ammeter) for frequencies ranging from dc to a few hundred hertz. A resistor in series with the two windings transforms the ammeter into a true rms voltmeter (electrodynamic voltmeter). The upper frequency limit of electrodynamic voltmeters is also a few hundred hertz but slightly

Thermoelectric Conversion Principle (Thermal Volt–Ampere Meters)

Analog multiplication related to the thermoelectric effect is achieved by using two thermoelectric converters in the con- where k_C is the constant of the converters. figuration depicted in Fig. 3. Currents i_1 and i_2 are propor-
Replacing Eq. (7) in (9) and Eq. (8) in (10) yields tional to the voltage and current involved in the power to be

*i*₂ $R_f \longrightarrow \rightarrow R_f$ $R_f \sim R_f$ *i*2 i_1 N i_1 i_A i_B U_A U_1 U_{B}

whose average product is to be measured, enter the circuit at opposite corners of the bridge. basic building block of that circuit.

portional to the rms values of the voltage and current in- measured. The two thermoelectric converters are coupled,

$$
i_{A} = \frac{1}{2}(i_{1} + i_{2})
$$
\n(7)

$$
i_{\rm B} = \frac{1}{2}(i_1 - i_2) \tag{8}
$$

lower than for electrodynamic ammeters.
Readers interested in typical values and specifications for
electrodynamic instruments, are directed to Refs. 4 and 5.

$$
U_{\rm A} = k_{\rm C} I_{\rm A_{\rm rms}}^2 = k_{\rm C} (i_{\rm A}^2)_{\rm av}
$$
 (9)

$$
U_{\rm B} = k_{\rm C} I_{\rm B_{\rm rms}}^2 = k_{\rm C} (i_{\rm B}^2)_{\rm av}
$$
 (10)

$$
U_{\rm A} = \frac{k_{\rm C}}{4} [(i_1 + i_2)^2]_{\rm av} = \frac{k_{\rm C}}{4} (i_1^2)_{\rm av} + \frac{k_{\rm C}}{4} (i_2^2)_{\rm av} + \frac{k_{\rm C}}{2} (i_1 i_2)_{\rm av} \tag{11}
$$

$$
U_{\rm B} = \frac{k_{\rm C}}{4} [(i_1 - i_2)^2]_{\rm av} = \frac{k_{\rm C}}{4} (i_1^2)_{\rm av} + \frac{k_{\rm C}}{4} (i_2^2)_{\rm av} - \frac{k_{\rm C}}{2} (i_1 i_2)_{\rm av} \tag{12}
$$

$$
U_1 = U_A - U_B = k_C (i_1 i_2)_{\text{av}} \tag{13}
$$

Equation (13) reveals that the dc voltage U_1 of Fig. 3 is proportional to the active power, provided that i_1 and i_2 themselves are proportional to the voltage and current in the circuit.

To assemble an active power meter, the circuit of Fig. 3 must be complemented with a dc voltmeter to measure U_1 and with a resistor to convert the voltage in the circuit into i_1 or i_2 .

To implement either an apparent power meter or an ammeter or a voltmeter, the considerations before about the corresponding electrodynamic meters are valid. Constructing a reactive power meter based on thermoconverters is also possi-**Figure 3.** Thermoelectric power meter using two identical thermo-
ple. All that is required is to include a circuit before that of
electric converters that have resistance R_c . The currents i, and i. Fig. 3 to produce a electric converters that have resistance R_f . The currents i_1 and i_2 , Fig. 3 to produce a lag of $\pi/2$ in the current representing the whose average product is to be measured, enter the circuit at opposite voltage

Figure 4. Hall effect analog power meter. The analog multiplication of the current and voltage comprised in the power is obtained by using the Hall effect principle. $B(t)$ is proportional to the current and *i* to the voltage in the circuit.

The thermoelectric analog multiplication just presented **Feedback Time-Division Multiplier Principle** permits assembling wideband multipower meters because **(FTDM Volt–Ampere Meters)** thermoelectric converters can be used at frequencies up to a
filme-division multiplication is a technique of analog multiplication few hundred megahertz.

and semiconductors but is particularly important in the lat- concept. Because of its positive characteristics, nowadays the ter. It consists of the appearance of a voltage u_H between two FTDM principle first introduced by Yokogawa Electric Works parallel faces of a parallelepiped semiconductor chip when the (YEW) is extremely popular among ma parallel faces of a parallelepiped semiconductor chip when the semiconductor is subjected to a current *i* and to a magnetic tronic volt–ampere meters. induction field *B* acting in the two other orthogonal direc- The basic component of the multiplier is the mark space

$$
u_{\rm H} = \frac{R_{\rm H}}{d} i B(t) \qquad (14) \quad \text{voltage is} \n\text{Fig. 5(b),}
$$

where R_H is the so-called Hall coefficient of the material and *d* is its thickness. Figure 4 represents the circuit configuration for the analog multiplication of a voltage and a current using the Hall effect principle. The magnetic field in the semiconductor is proportional to the current in the circuit and the where k_1 and k_2 are two constants. By multiplying the MSA current in the semiconductor is proportional to the voltage in output voltage u' with the other voltage u_2 occurring in the the circuit. Thus, the time averaging of u_{H} , which can be done product, one obtains a voltage u'_{0} : by mechanical or electrical means, is proportional to the active power. In the first case, a d'Arsonval meter is usually *u* used. In the second, an electronic integrator (active low-pass

filter) is the more common solution.
As in the case of thermoelectric conversion, the Hall effect
principle can support the design of a multipower meter for
and a low-pass filter [Fig. 5(a)]. measuring active, reactive, and apparent power. However, as far as we know, only active power meters have been assem- **The MSA Converter.** The MSA converter represented in Fig. bled and used. The direct field sensor (DFS) from Landis & 5(b) was also introduced by YEW and is used, for instance, in Gyr is an example of applying the Hall effect to measuring its digital wattmeter model 2504. active power and also electrical energy. DFS incorporates a The operation of the converter is as follows. A comparator (3) very good electromagnetic immunity, in part because of its tor output voltage is negative, and S_1 changes to $-U_{ref}$.
inherent galvanic isolation; and (4) the possibility of integrat-
If the frequency of the triangu inherent galvanic isolation; and (4) the possibility of integrat-

Hall Effect Principle (Hall Effect Volt–Ampere Meters) is equal to the product of the pulse width and pulse height.
The circuit represented in Fig. 5(a) is a possible hardware The Hall effect is a phenomenon that occurs in conductors solution to implement an analog multiplier according to this

tions. The relationship among the three quantities is given by amplitude (MSA) converter [Fig. 5(b)]. In this converter, the duty cycle of a constant amplitude and period rectangular voltage is modulated by a voltage u_1 . Thus, and referring to

$$
T = T_1 + T_2 = k_1 \tag{15}
$$

$$
T_1 - T_2 = k_2 u_1 \tag{16}
$$

$$
u_0' = u_2(T_1 - T_2) = k_2 u_1 u_2 \tag{17}
$$

Hall sensor, a custom chip, and additional circuit elements. compares a voltage u_c with a triangular waveform u_t (this Its main positive characteristics are: (1) accuracy better than voltage is internally generated in the model previously men-1%; (2) good stability and the possibility of automatic calibra- tioned). When $u_c > u_t$, u' is positive $(u' > 0)$ and S_1 switches tion, which leads to highly reliable maintenance-free meters; so that u_0 becomes equal to $+U_{\text{ref}}$. When $u_{\text{c}} < u_{\text{t}}$, the compara-

ing functions, such as radio telemetry. higher than the frequency of u_1 and u_2 , then these voltages

Figure 5. Time division multiplier: (a) amplitude modulator and low-pass filter. *u'*, the output voltage of the MSA converter, represented in (b) , is multiplied by u_2 , and the average of this product is achieved by using a low-pass filter; (b) mark space amplitude converters. The duty cycle of a constant amplitude and period rectangular voltage, the output voltage *u*, is modulated by a voltage u_1 .

can be considered constant compared with u_t . Thus, in the $(i_d)_{av} = 0$. The action of the closed steady state, u_c should be periodic and $(i_d)_{av} = 0$ because u_c is instrumental for that purpose. steady state, u_c should be periodic and $(i_d)_{av} = 0$ because u_c is instrumental for that purpose. the integral of this current. Thus,

$$
T_1 \left(\frac{u_1}{R} - \frac{U_{\text{ref}}}{R}\right) + T_2 \left(\frac{u_1}{R} + \frac{U_{\text{ref}}}{R}\right) = 0 \tag{18}
$$

$$
(T_1+T_2)\frac{u_1}{R}=(T_1-T_2)\frac{U_{\text{ref}}}{R} \eqno{(19)}
$$

$$
(T_1-T_2)=T\frac{u_1}{U_{\rm ref}}=k_2u_1\eqno(20)
$$

$$
T_1 = \frac{T}{2} \left(1 + \frac{u_1}{U_{\text{ref}}} \right) \tag{21} \quad \text{and thus,}
$$

This means that T_1 is continuously adjusted by comparing u_c and u_t . If the average value of i_d is not zero, then the voltage u_c is not periodic but increases or decreases, changing the relationship between T_1 and T_2 . The steady state occurs when $(i_d)_{av} = 0$. The action of the closed-loop set comparator switch

The Amplitude Modulator and Low-Pass Filter. Referring to Fig. 5(a), and taking into consideration that the output voltage *u'* of the comparator of the MSA converter controls switch S_2 ,

$$
u'_{0} = \begin{cases} +u_{2} & \text{if } u' < 0\\ -u_{2} & \text{if } u' > 0 \end{cases}
$$
 (22)

$$
u_0 = (u'_0)_{av} = \frac{1}{T} \int_T u'_0(t) dt = \frac{1}{T} [u_2 T_1 - u_2 (T - T_1)]
$$

= $\frac{1}{T} [2u_2 T_1 - u_2 T] = \frac{1}{T} \left[2u_2 \frac{T}{2} \left(1 + \frac{u_1}{U_{ref}} \right) - u_2 T \right] = \frac{u_1 u_2}{U_{ref}}$ (23)

The FTDM principle for analog multiplication can be used for **Logarithmic Conversion Principle** active, reactive, or apparent power measurement. Naturally,
the voltage and current of the circuit test must be conditioned
before they are applied to the multiplier input. In the case of
the current, the conditioner must converter using, for instance, a shunt resistor and an operational amplifier.

For active power measurement, the output voltage u_0 of the circuit of Fig. 5(a), must be averaged by another low-pass

For reactive power measurement, the voltage of the circuit plifiers. Applying the inverse function to \pm must be lagged by $\pi/2$ before it is input to the FTDM of the two quantities a and b is given by test must be lagged by $\pi/2$ before it is input to the FTDM. That lag is easily obtained from electronic circuitry and, thus, it is performed after the voltage input conditioner. The output of the multiplier must also be low-pass filtered to yield the

converters placed between the input conditioners and the ule uses two identical logarithmic converters, whose output multiplier. The product of the two dc voltages output by the voltages are related to the input voltages by rms converters indicates the value of the apparent power.

The measurement of voltage and current is also easily implemented by using the FTDM multiplier. For that purpose, it is sufficient to connect the output of the voltage conditioner or of the current conditioner to both inputs of the multiplier.

Several FTDM volt–ampere meter manufacturers, such as YEW, Dranetz, Landis & Gyr, and Sclumberger offer energy where counters based on the FTDM multiplying principle. In fact, all that it is required is to add an integrator to the active power meter. In the YEW wattmeter model 2504, the integrator is a stand-alone instrument (YEW 2513).

$$
\log(ab) = \log a + \log b \tag{24}
$$

the circuit of Fig. 5(a), must be averaged by another low-pass the logarithmic conversion of two quantities transforms its product into a sum, easily achieved by using operational am-
For reactive power measurement the vol

$$
\log^{-1}[\log(ab)] = ab = \log^{-1}(\log a + \log b)
$$
 (25)

reactive power value.
Figure 6 presents a circuit based on logarithmic and antiloga-
For apparent power, the easiest solution involves two rms rithmic converters which multiplies u_1 and u_2 . The first modrithmic converters which multiplies u_1 and u_2 . The first mod-

$$
u_{01} = -k_1 \log_{10} \frac{u_1}{R I_{\text{ref}}}
$$

$$
u_{02} = -k_1 \log_{10} \frac{u_2}{R I_{\text{ref}}}
$$
 (26)

$$
k_1 = \frac{R_{\rm T} + R_1}{R_{\rm T}} \cdot \frac{kT}{q} \frac{1}{0.434}
$$
 (27)

Figure 6. Analog multiplication using logarithmic and anti-logarithmic amplifiers: $u_{01} \propto \log \frac{u_{01}}{u_{01}}$ u_1 ; $u_{02} \propto \log u_2$; $u_{03} \propto \log u_1 + \log u_2$; $u_0 \propto \log^{-1} (u_{03}) = \log^{-1} (\log u_1 + \log u_2) = u_1 u_2$.

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In Eq. (27), *k* is the Boltzmann constant (1.38 \times 10⁻²³ $J \cdot K^{-1}$), *T* is the absolute temperature of the semiconductors junctions, and *q* is the charge of the electron $(1.6 \times 10^{-19} \text{ C})$.

The operational amplifier of the intermediate stage provides

$$
u_{03}=-k_1\log_{10}\frac{u_1u_2}{R^2I_{\rm ref}^2}\eqno(28)
$$

The final module performs the antilogarithmic operation. The output voltage is proportional to the antilogarithmic of the **Figure 7.** Digital power meter: after digitizing the voltage and cur-

$$
u_0 = RI_{\text{ref}} \log_{10} \left[k_1 \times \frac{\log_{10} \left(\frac{u_1 u_2}{R^2 I_{\text{ref}}^2} \right)}{k_1} \right] = \frac{u_1 u_2}{R I_{\text{ref}}} \tag{29}
$$

but leads to instruments with moderate performance. In fact, led to the replacement, whenever possible, of analog measur-
the existence of logarithmic and antilogarithmic amplifiers ing methods by digital measuring methods the existence of logarithmic and antilogarithmic amplifiers ing methods by digital measuring methods. This trend is also
and of log/antilog multipliers as integrated circuits (e.g., felt in the domain of low-frequency powe and of log/antilog multipliers as integrated circuits (e.g., felt in the domain of low-frequency power and energy mea-
Burr-Brown 4127, Burr-Brown 4302) simplifies designing and surements. As already mentioned active power Burr-Brown 4127, Burr-Brown 4302) simplifies designing and surements. As already mentioned, active power, reactive constructing power meters. However, such components are ei-
nower and apparent power are measured digitally constructing power meters. However, such components are ei-
the power, and apparent power are measured digitally in two
ther inexpensive and moderately accurate or they perform steps. First, the voltage and current are sim ther inexpensive and moderately accurate or they perform steps. First, the voltage and current are simultaneously con-
well, but then they are quite expensive. For that reason volt-
yerted to digital words, and then the re well, but then they are quite expensive. For that reason volt– verted to digital words, and then the resultant data is digi-
ampere meters based on this type of component are not popu- tally processed by some processing un ampere meters based on this type of component are not popu-
lare and some processing unit. This means that two
lar and are limited to applications demanding only low accu-
equally important components are involved in the o lar and are limited to applications demanding only low accu-
racy (not less than 1%).
of this type of power meter, the bardware and the software

As long as the multiplier block operates with positive and The routines that constitute the software can be quite differ-
negative input voltages (four-quadrant multiplier), its func-
ent from instrument to instrument. The negative input voltages (four-quadrant multiplier), its func-
tionality, from the viewpoint of the terminal voltages, is simi-
nents of the instruments have however some similarities tionality, from the viewpoint of the terminal voltages, is simi-
lart to that of the FTDM. This means that the considerations Figure 7, which represents the implementation used by Yokolar to that of the FTDM. This means that the considerations Figure 7, which represents the implementation used by Yoko-
in the previous section regarding circuitry for measuring the σ _{gwa} in models WT110/WT130, gives in the previous section regarding circuitry for measuring the gawa in models WT110/WT130, gives a good picture of the different types of power, voltage, current and even energy are building blocks of a typical low-frequenc different types of power, voltage, current and even energy are building blocks of a typical low-frequency multipower meter.
This instrument consists of various sections: input circuits

$$
(a+b)^2 - (a-b)^2 = 4ab \tag{30}
$$

able as integrated circuits. The so-called transconductance

DIGITAL POWER METER

The digitization of low-frequency electrical voltages and currents is presently achieved simply and at low cost. It origi- Although the measurement of active power is important in nated from low cost, high-performance, and reliable analog different frequency domains, reactive power and apparent and digital integrated circuits. The basic blocks of a digitizing power measurements are required basically in power sys-

input voltage rent, the CPU reduces the data to compute the active, reactive, or apparent power.

system are naturally the sample-and-hold and the analog-todigital converter. The performance of digitizing systems is highly dependent on the performance level of those components. Notwithstanding, the advances in digital circuits for Implementing power meters using this principle is fairly easy control, data processing, data storage, and data transmission but leads to instruments with moderate performance. In fact, led to the replacement, whenever poss ry (not less than 1%). of this type of power meter, the hardware and the software.
As long as the multiplier block operates with positive and The routines that constitute the software can be quite differ-

This instrument consists of various sections: input circuits (voltage and current input circuits), a digital signal processor **Other Solutions for Electric Analog Multiplication** (DSP), a central processing unit (CPU), a display and inter-

Two other types of solutions for analog multiplication of two
voltage input conditioner, the input voltage is atten-
voltages (or currents) are worth a short discussion.
The first has its origin in the following relationsh α + *b*) verted into a voltage by using a shunt resistor. Then the voltage is amplified and formalized by an operational amplifier The circuitry used to implement this idea also involves, in before entering another ADC converter. The outputs from general, logarithmic amplifiers, and thus this type of multi- both ADC converters are sent to the digital signal processor plier is considered a variation of the log/antilog multiplier (DSP). The DSP reduces the data to produce the values of mentioned in the preceding paragraph. voltage, current, and active power for each sampled datum In the second type we include several different multipliers sent from the ADC converter. After processing a certain numthat are based on different principles but have two aspects in ber of sets of data, computation of the apparent power, reac-
common. They are electronic multipliers, and they are avail- tive power, power factor, and phase common. They are electronic multipliers, and they are avail- tive power, power factor, and phase angle starts. Computa-
able as integrated circuits. The so-called transconductance tional results are sent to the CPU where c principle (7,8) is an example of a principle used, for instance, as range conversion and scaling, are carried out. Display and in Burr-Brown MPY 100 and in Harris ICL8013. outputs are also controlled by the CPU. The output goes to a readout driver (display).

GENERAL CONSIDERATIONS

tems. The measuring principles discussed take that into account.

In power systems, it is often necessary to measure the three-phase power and energy. It is worth mentioning that some measuring methods, such as the Aron method, avoid the use of three volt–ampere meters. Interested readers are directed to Chapter VI of Ref. 4 and Chapter 6 of Ref. 5.

Excluding electrodynamic volt–ampere meters, all of the analog multiplication power meters considered are built around a block that produces a voltage (or current) proportional to the product of the current and voltage of the circuit under test. The meter hardware must be complemented with a voltage (or current) measuring device and with input conditioning circuitry whose composition depends on the quantity to be measured by the power meter and on its accuracy. For measurements in the range 45 to 65 Hz with accuracy better than 0.5%, the input circuits include current and voltage transformers or, less often, voltage dividers and current shunts. These components, that reduce the amplitude of the input current and voltage to levels accepted by the electronic components, are the front end of the meters and much of the meters performance depends on the metrological characteristics of such passive components. The reader should take the solutions considered in the text as guidelines for understanding the internal composition of those types of volt–ampere meters.

The following values are typical for the accuracy of volt– ampere meters: analog type—0.1% to 2% full-scale value (FSV); digital type—0.2% to 2% FSV. Presently, the standards used for calibration are usually of the FTDM type (e.g., YEW 2855) whose accuracy is around 0.02% FSV.

In this article, we elected to separate volt–ampere meters into analog and digital types according to the principle of measurement they use, but this classification is not universal. In fact, most manufacturers use the term digital whenever the meter displays the measurement numerically. The reader must be aware of this fact when consulting different manufacturers' catalogs.

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