More in the speed to the speed to the speed of electric motors that drive industrial equipment. Motor speed is best and

most conveniently controlled by adjusting motor supply volt-

age or current and/or frequency. Variab

Modern VSDs are almost exclusively based on static solid-
state semiconductor power electronic devices. The power elec-
tronic devices function as controllable power switches be-
tween the utility source and the motor. Thr switching, the desired motor voltage or current characteris-
tics can be synthesized from a constant dc or ac source. We switchgears, surge suppressors, transformers, and intics can be synthesized from a constant dc or ac source. We switchgears, surge suppressors, transformers, and in-
all have daily experience with some types of electronic power put and output filters for harmonics and wavef all have daily experience with some types of electronic power put and α converters for example, actode adapters dimmer switches ditioning converters, for example, ac-to-dc adapters, dimmer switches, and power supplies in PCs and other home electronics. They all involve converting 50 Hz or 60 Hz household power into Software is also an essential part of digitally controlled VSDs. some other form of power by electronic switching. In principle, A drive today is almost entirely operated by executing soft-VSDs based on power electronic devices function much the ware programs usually stored in read-only memory in prosame way, except that they are power converters designed cessors as firmware. Software covers all facets of drive operaspecially for variable speed motor applications. The power tion: control, communication, data processing, diagnostics,

electronic devices in VSDs generally have much higher power ratings than home electronics.

Before the advent of power semiconductors, electromechanically based VSDs dominated for many years. An example is the classical Ward-Leonard dc motor drive that uses a constant speed induction motor driving a dc generator to create a variable dc voltage by controlling the generator's magnetic field. The electromechanically based VSDs have been supplanted because equipment based on power semiconductor electronic devices generally has lower cost, higher efficiency, smaller size, higher reliability, better maintainability, higher dynamic performance, and no moving parts.

Power electronic switching devices are essential to a variable speed drive. In addition, other hardware components are often necessary as integrated parts of a drive, depending on the converter topology. The hardware for VSD can be classified into four major categories:

- 1. Power conversion circuit components, including
	- power electronic devices
	- auxiliary switching and protection circuits, such as snubbers, commutation circuits, fuses, and grounding networks
	- reactors and capacitors as energy storage components
- 2. Mechanical components, including
	- heat sinks for power electronic devices, consisting of extruded aluminum heat sinks and sometimes heat pipes for air-cooled drives, and liquid pipes, reservoirs, and heat exchangers for liquid-cooled drives
	- blowers or fans for forced air cooled drives
	- bus bars, connectors, and harnesses
- 3. Control hardware, including
	- gate drive circuits that generate on-off gate signals for thyristors and power transistors
- sensors for voltage, current, and speed measurements, **VARIABLE SPEED DRIVES** which are often required for closed-loop controls
	-
- forms D/A and A/D transformation, signal receiving, **VSD COMPOSITION VSD COMPOSITION** *Processing, and transmitting.* **•** communication I/O circuits interfacing with external
	-
	-
	-

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and protection. It is becoming an increasingly bigger part of \cdot GTOs are current triggered devices like SCRs but are a VSD as digital technology advances and more sophisticated also turned off by gate pulses. They also have high power operating algorithms are being developed and implemented. capabilities and handle voltages up to several kilovolts

power semiconductor technology and digital control and com- time is in the range of microseconds to tens of microsecputer technology. Because these technologies are progressing onds. GTOs are used in high power drives. rapidly, VSD technology has been changing fast, new and bet- • BJTs are current-controlled devices, and their base curter drives are being developed continually, and new applica-
tions are being pursued. A major factor in the development
on-state. Typical switching time for BJTs ranges from of VSD technology is customer demand for additional VSD hundreds of nanoseconds to microseconds. Their voltage
performance to compensate for deficiencies in motor, power ratings are up to 1000 V and current ratings to seve performance to compensate for deficiencies in motor, power ratings are up to 1000 V and current ratings to several
factor, and electrical noise issues.

All commercially available power electronic devices are used
in motor drives. The three basic types of power electronic de-
source of tens to hundreds of nanoseconds. Their voltage ratings
in motor drives. The three basic transistor in that the thyristor is a latching device that requires only a pulse gate signal to turn on or off. The transistor **VSD APPLICATIONS** is a nonlatching device that requires a continuous on or off gate signal for conduction or blocking. Each category of power VSDs are frequently found in every industry sector and most electronic devices has a number of variants. The commonly commonly in the used power transistors in VSDs include bipolar junction transistors (BJTs), metal oxide semiconductor field effect transis-
tors (MOSFETs), and insulated gate bipolar transistors boiler fans and pumps chippers, refiners and convevors tors (MOSFETs), and insulated gate bipolar transistors boiler fans and pumps, chippers, refiners, and conveyors
(IGBTs). The commonly used thyristors include phase-con-
second industry for rolling mill stands rools and win (IGBTs). The commonly used thyristors include phase-con-
trolled thyristors (SCRs), gate-turn-off thyristors (GTOs), and
MOS-controlled thyristors (MCTs)
whining industry for excavators, conveyors, and grinding
mining indu

- mills Power diodes, similar to their counterparts in microelec- cement industry for kiln drives and fans and conveyors tronics, conduct when they are forward biased. Commu- petroleum and chemical industry for pipeline compres- tation, the process of turning-off and transfer of current sors and pumps, oil well drilling equipment (draw works, from one diode to another, occurs when diodes are re- top drives, mud pumps, and cement pumps), water and verse biased and their current decreases to zero. Diodes wastewater pumps, and rubber and plastics equipment are usually used in rectifiers, commutative circuits, and (extruders, inlet pumps, pelletizers, and mixers) protection circuits. Their ratings are up to several kilo- transportation industry for locomotive traction, ship pro- volts in voltage and several kiloamperes in current. pulsion, aircraft generators, and off-highway vehicles SCRs are current triggered switches and can be turned automotive industry for electric vehicles, dynamometers, on by gate current pulse signals when they are in the and wind tunnels forward-blocking state. SCRs can only be turned off like
- diodes by reverse bias. Their switching speed is relatively **•** appliance industry for washing machines, air conditionlow. They have large voltage and current handling capa- ers, and HVAC bilities with voltage ratings up to 5 kV to 7 kV and cur- • electric utility industry for turbine starters, boiler and rally commutated, high-power applications. turbines
- The technology drivers behind VSD development are and current up to several kiloamperes. Their switching
	- on-state. Typical switching time for BJTs ranges from hundred amperes. BJTs are used in low-voltage, lowpower drives.
- MOSFETs are voltage-controlled devices that require **POWER ELECTRONIC DEVICES IN VSDs** continuous positive gate voltage to keep them conducting
	-
	-

-
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-
- rent ratings up to 4 kA. SCRs are usually used in natu- cooling tower fans and pumps, wind turbines, and micro-

The drive size ranges from a fraction of a horsepower in an \cdot Speed regulation response: characterized through re-
appliance to tens of megawatts in rolling mills. The type of sponse time to a step change in speed comma appliance to tens of megawatts in rolling mills. The type of sponse time to a step change in speed command. A typi-
loads in VSD applications are often variable torque loads, cal industrial application requires a 15 rad/s such as fans and pumps whose torque is a function of speed or speed regulator. constant torque loads whose torque is independent of speed. • Overload capability

CLASSIFICATION OF VSDs • Efficiency

Based on the types of motors they drive, there are two general

categories of VSDs, dc drives and ac drives. Ac drives include

induction motor drives and synchronous motor drives. Switch-

ing reluctance motor (SRM) drive (PM) synchronous motor drives (known as dc brushless mo-
tors) are two important variants of ac synchronous motor
tors) are two important variants of ac synchronous motor
power disturbance. tors) are two important variants of ac synchronous motor drives.

VSDs can be further classified in many other ways: **DC DRIVES**

- Based on input power source, a VSD can be either dc fed **Dc Motor Speed Control** or ac fed.
-
-
-
- Based on commutation techniques, a VSD can be line a commutated, load-commutated, or force-commutated.
- Based on cooling technology, a VSD can be naturally α cooled, forced air-cooled, or liquid-cooled.
-
- Based on braking capability, a VSD can be regenerative Its the mechanical angular speed of the motor.

Its steady-state torque-speed characteristic is given by its steady-state torque-speed characteristic is given by
- Based on applications, a VSD can be a stand-alone drive or a system drive where multiple drives are used in a coordinated system, such as paper machines or rolling mill stands. System drives usually require highly pre-

Equation 4 indicates that three basic means for dc motor

speed control are by armature voltage, flux or field current, or

There are three basic categories of performance indexes for VSD, those related to speed control, those related to power converters, and those related to interface with utility power source and motor. Some of the most important are listed here (3–5):

• Speed range: the ratio between maximum speed and minimum speed. A related parameter is field-weakening speed range defined as the ratio of top speed to rated speed. **Figure 1.** Schematic representation of dc motors.

- textile and man-made fiber industry for extruders, Speed accuracy: the error in speed as a percentage of pumps, fans, HVAC, and conveyors. The rated speed. For digitally controlled and tachometerbased VSDs, the steady-state error is as low as 0.01%.
	- cal industrial application requires a 15 rad/s or less
	-
	- Starting torque
	-
	-
	-
	-
	-

Fraction of a denoted on rolling motor

Assed on voltage ratings, a VSD is low voltage if lower

than 600 V or medium voltage if above 600 V.

Based on converter topology, there are phase-controlled
 \bullet Based on convert

$$
\Phi = k_{\rm f} I_{\rm f} \tag{1}
$$

$$
T = k_a \Phi I_a = k_a k_f I_f I_a \tag{2}
$$

$$
E = k_e \omega_m \Phi = k_e k_f \omega_m I_f = V_a - I_a R_a \tag{3}
$$

• Based on control technologies, a VSD can be analog or
digital, open-loop controlled or close-loop regulated, with
sensors or sensorless, and in the case of ac drives, scaler-
controlled or vector-controlled.
is the arma

$$
\omega_{\rm m} = \frac{V_{\rm a}}{k_{\rm e} k_{\rm f} I_{\rm f}} - \frac{R_{\rm a}}{k_{\rm e} k_{\rm a} k_{\rm f}^2 I_{\rm f}^2} T \tag{4}
$$

armature resistance. The most common approach in dc VSD **VSD PERFORMANCE INDEXES** applications is combining voltage and field control. Below

Field **Armature** *I*a *I*f

Figure 2. Topology of phase-controlled dc

rated speed, ω_m is adjusted by armature voltage, and field cur- ogies for such converters where SCRs are the main switching angle control can be applied to PM dc motors to extend the speed range beyond the rated speed (6).

Based on the principles of dc motor speed control, a dc VSD in general should be an adjustable dc voltage source plus a controllable field exciter.

Phase-Controlled Rectifiers (3,7)

Phase-controlled rectifiers convert line-frequency ac voltage to controlled dc voltage. Figure 2 shows several possible topol-**Figure 3.** De equivalent circuit of line-frequency converters.

rent I_f and therefore motor flux Φ are kept constant at their devices. If these SCRs are substituted by diodes, these conrated values. Above rated speed, ω_m is adjusted by I_f such verters would be the familiar diode rectifier bridges that con-
that V_a remains constant at its rated value. The technique of vert line-frequency ac to con vert line-frequency ac to constant dc. In the case of diode recdecreasing the field current to extend speed range beyond tifiers, a diode conducts as soon as it enters the forward bias rated motor speed is termed field-weakening control. The state. An SCR conducts under forward bias only when a gate field-weakening region is often called the constant power re- pulse is also fired. By controlling the firing angle α , defined gion because the power holds constant for a constant motor as the delay angle from its initial forward bias point, the current when the voltage is constant. Below rated speed is equivalent dc voltage of the rectifier can be controlled. Figure the constant torque region where the torque holds constant 3 shows a dc equivalent circuit for converters in Fig. 2. Circuit for a constant motor current when the flux is constant. Power parameters for different converter parameters for different converter topologies are listed in Ta-

Table 1. Dc Equivalent Circuit Parameters*^a*

Converter Type	$R_{\rm d}$	$\boldsymbol{V}_{\text{d}}$	$V_{\rm do}$	
Fig. $2(c)$	$\frac{2}{\pi}\omega L_{c}$	$\frac{V_{d0}}{2}(1 + \cos \alpha)$	$\sqrt{2}V_{\rm rms}$ π	
Fig. $2(e)$	$\frac{2}{\pi}\omega L_c$	$V_{\rm do}$ cos α	$\sqrt{2}V_{\rm rms}$ π	
Fig. $2(b)$	$\frac{3}{\pi}\omega L_c$	$\frac{V_{d0}}{2}(1 + \cos \alpha)$	$3\sqrt{6}V_{\rm ln\mbox{-}rms}$	
Fig. $2(d)$	$rac{3}{\pi} \omega L_c$	$V_{\rm do}$ cos α	$3\sqrt{6}V_{\rm ln-rms}$ π	

^a Ref. 7.

ages for fully-controlled converters in Fig. 2(d) and (e) can lar. When the motor is in the braking mode alternate polarity. All four topologies allow only unidirectured flows into the source, as shown in Fig. 4(b). alternate polarity. All four topologies allow only unidirectional dc current into the motor armature. Therefore, with semicontrolled converters, a dc motor can operate in only one quadrant, the motoring operation quadrant, as shown in Fig. $2(g)$. With fully-controlled converters, the motor can work in two quadrants: motoring and regenerating, as shown in Fig. 2(h). However, to have braking capability, the motor field current must alternate polarity in two-quadrant converters.

Figure 2(f) shows a dual fully-controlled converter topology with both forward and reverse current capability that can operate in all four quadrants, as shown in Fig. 2(i). It is a popular configuration and provides full regenerating capability because it does not require a polarity change for field current and has superior dynamic performance. When a phase-controlled rectifier is regenerating, it is said to be working in an inverter mode.

The output current of a phase-controlled rectifier normally contains ac ripples that are multiples of ac line frequency, and a series smoothing reactor is often required to filter out these current ripples.

Phase-controlled dc drives always have lagging power factors. They generate harmonics in input ac line currents because of discrete switching. Because the switching and commutation are based on ac line frequency, these harmonics are characteristic harmonics or multiples of line frequencies.

Dc Choppers (1)

Dc-fed dc converters are called choppers. A PWM scheme is usually used to control voltages applied to dc motors. The dc sources in VSD applications are usually phase-controlled or PWM ac-to-dc fully regenerative sources and can be common sources shared by multiple dc-fed drives.

A four-quadrant operation chopper is shown in Fig. 4(a). The chopper has four switches and four antiparallel diodes. The two switches on the same leg, $S1+$ and $S1-$, or $S2+$ and $S2-$, do not turn on or off simultaneously. When $S1+$ is on and S1- is off, $V_{AN} = V_d$. When S1+ is off and S1- is on, $V_{\rm a} = V_{\rm AV} - V_{\rm B}$. Therefore $V_{\rm AN}$ is independent of the direction **Figure 4.** Chopper topology for dc drives.

of I_a , and

$$
V_{\rm AN} = V_{\rm d} \cdot (\text{duty ratio of S1 +}) \tag{5}
$$

A similar relationship exists for V_{BN} :

$$
V_{\rm BN} = V_{\rm d} \cdot (\text{duty ratio of S2 +}) \tag{6}
$$

Because $V_\text{\tiny a}=V_\text{\tiny AN}-V_\text{\tiny BN}$, the terminal voltage of a dc motor is either positive or negative. The armature current I_a can also be dual directional. The possible operating quadrants are shown in Fig. 4(a). Because the source current I_d changes direction instantaneously, it is important that the source has a low internal impedance. In practice this is usually done by installing a large filtering capacitor in parallel with the dc source.

ble 1. Clearly, the terminal voltage can be controlled by SCR Figure 4(b) shows a two-quadrant chopper, where one of firing angles. The range for α is from 0° to nearly 180°. The two switches is on at any time to firing angles. The range for α is from 0° to nearly 180°. The two switches is on at any time to keep the output voltage independent of the direction of I_a . The motor terminal voltage independent of the direction of Table 1 indicates that semicontrolled converters in Fig. independent of the direction of I_a . The motor terminal voltage
a) and (c) have only positive de quanti voltages whereas volt-
 V_a can be controlled in magnitude 2(b) and (c) have only positive dc ouput voltages whereas volt- V_a can be controlled in magnitude but always remains unipo-
ages for fully-controlled converters in Fig. 2(d) and (e) can lar. When the motor is in the bra

One-quadrant operating topology

When the motor speed is unidirectional and no motor braking is needed, the topology of Fig. 4(c) can be used. Operation is limited to the first quadrant as in Fig. 4(c).

Choppers use diodes and controllable switches with turnoff capabilities. Depending on power ratings, the devices can be BJTs, MOSFETs, GTOs, IGBTs, or MCTs.

Field Exciters

Field exciters provide dc excitation currents for separately excited dc motors. They themselves are usually phase-controlled ac-to-dc rectifiers with current regulating capability.

Although there are many types of ac VSDs, invariably they speed control (3). are some type of variable-frequency and variable-voltage (or current) converter because ac motor speed control always involves frequency control and voltage (or current) control. torque and speed relationship for a three-phase motor is given

by **Ac Induction Motor Speed Control.** Induction motors are electrically made up of balanced multiphase stator windings and shorted rotor windings. A typical induction motor can be represented by the equivalent circuit in Fig. 5, in which, R_s and L_{ls} are stator winding resistance and leakage inductance, R_r and L_{lr} are rotor winding resistance and leakage inductance referred to the stator winding, L_m is the equivalent airgap magnetizing inductance, *f* is the stator source frequency, Figure 6 shows a family of torque-speed curves based on Eq. and f_{sl} is the slip frequency. For a motor with *p* poles, the slip (8) for various stator voltages. The torque in the curves are frequency is given by normalized on maximum pull-out torque at rated voltage, and

$$
f_{\rm sl} = \frac{\omega_{\rm s} - \omega_{\rm m}}{\omega_{\rm s}} f = sf \tag{7}
$$

$$
\omega_s = \frac{p}{2} \omega_e = \frac{p}{2} 2\pi f
$$

is the synchronous mechanical speed, ω_e is the angular elec-seldom used in VSDs. trical frequency, ω_m is the rotor speed, and *s* is the per unit Equation (8) shows that changing stator voltage changes

It is necessary to maintain slip for an induction motor to gen- ening control. It is of limited use in fan or pump type loads. erate torque. Slip power or rotor resistance control applies only to

The resistance $((f - f_{sl})/f_{sl})R_r$ in Fig. 5 is the equivalent motor load. Neglecting magnetizing branch in Fig. 5, the ternal terminals. The basic idea is to change the slip speed by

Ac Motor Speed Control Figure 6. Induction motor torque–speed curves with stator voltage

$$
T = \frac{2}{p\omega_e} \frac{3V_s^2 \frac{R_r}{s}}{\left(R_s + \frac{R_r}{s}\right)^2 + \omega_e^2 (L_{1s} + L_{1r})^2}
$$
(8)

the speed is normalized on rated synchronous speed. Equa $t_{\rm sl} = \frac{\omega_{\rm s} - \omega_{\rm m}}{\omega_{\rm s}} f = sf$ (7) tion 8 clearly indicates that there are four basic means for according to $\omega_{\rm s}$ for a control: pole switching, stator voltage control, rotor resistance or slip power control, and frequency where $control.$

> Pole switching physically alters the number of poles *p* for a motor through external switchgear. Because this changes the synchronous speed ω_s , the motor speed also changes for a given load. Pole switching is a discrete speed control method

slip. $s = s$ speed. Figure 6 shows the speed control characteristics with Induction motors operate through magnetic fields rotating a fan type load. Because motor flux is proportional to voltage at ω_s that induce current in the rotor winding rotating at ω_m . at a given frequency, the voltage control method is flux-weak-

> *f* wound rotor induction motors whose rotor windings have exfeeding the slip energy back to the supply, converting it to additional useful mechanical power, or dissipating it in a resistor bank (7). This method is used in special VSD applications.

Frequency control changes supply frequency *f* and therefore motor synchronous speed ω_s , to control motor speed. It is often called V/Hz control because motor voltage is adjusted proportionally as *f* changes to maintain constant motor flux and maximize motor utilization. It can be shown from Eq. 8 that the maximum torque under a constant V/Hz ratio is **Figure 5.** Steady-state induction motor equivalent circuit. nearly independent of *f*. The principle of V/Hz speed control

is shown in Fig. 7. Below rated frequency, V/Hz ratio is main-
tained constant, and the motor operates in the constant
torque control for an LCI is by regulating the dc link cur-
tained constant, and the motor operates in

Ac Synchronous Motor Speed Control. Three types of synchronous experience in their four-quadrant operation of the synchronous motors based on rotor configuration are wound field
winding, permanent magnet, and reluctance. T does not run at all. The torque of a synchronous motor can be
Cycloconverter Drives (1,3,7,9–11) written as (27)

$$
T = \frac{3p}{2} \left[\frac{EV_{\rm s}}{\omega_{\rm e}^2 L_{\rm d}} \sin \delta + \frac{V_{\rm s}^2}{2\omega_{\rm e}^2} \left(\frac{1}{L_{\rm q}} - \frac{1}{L_{\rm d}} \right) \sin 2\delta \right]
$$
(9)

produced by field current, V_s is the terminal voltage, ω_e is the the cycloconverter is a circulating current type. Otherwise it electrical frequency, L_d and L_q are constant motor direct and is a noncirculating type. Each dc bridge supplies one phase of quadrature inductances, and δ is the load angle between vec- a three-phase induction or synchronous motor. tors E and V_s . Because E and V_s are proportional to frequency The operating principle of a cycloconverter is shown in Fig. ω_e under a given flux, the constant V/Hz speed control for 11. VSD motor control generates a sinusoidal reference phase induction motors applies to synchronous motors. voltage of a certain amplitude and frequency. By properly

commutated and produces a controlled dc voltage. The dc link inductor turns the line-side converter into a current source to the motor or load-side converter. The load converter operates normally in the inversion mode. The drives usually use standard SCRs as switching devices.

Load commutation is made possible by the reactive power capability of synchronous motors. The motor must operate at a sufficient leading power factor to overcome the commutating reactance. Typical waveforms of an LCI load converter are shown in Fig. 9. The line-to-line voltages are motor internal emfs generated by the field exciter.

An LCI must be force-commutated at low speeds (usually less than 10% of rated speed) because there is not enough voltage to commutate the load converter (3). In this mode, **Figure 7.** Induction motor torque-speed curves with V/Hz speed con-
trol (3) chut off de link gumpnt. As seen as the conduction SCPs in shut off dc link current. As soon as the conducting SCRs in the load converter have turned off, the next pair can be selected and the source converter current can be restored.

A cycloconverter is a direct ac converter, that is, there is no intermediate energy storage. The configuration of a typical line-commutated cycloconverter drive is shown in Fig. 10. It uses three SCR-based, four-quadrant dc bridges. With a curwhere p is the number of poles, E is the motor internal emf rent balancing reactor between positive and negative bridges,

controlling the firing of each SCR, the cycloconverter cuts **LCI Drives (1,7,8)** small segments from the incoming line voltage waveforms The topology of an LCI synchronous motor drive system is
shown in Fig. 8. It has two two phase-controlled converter
bridges and a dc link inductor. The source converter is line-
periodical reference voltage. A cycloconvert variable-voltage, variable-frequency source. The cycloconverter shown in Fig. 11 is a noncirculating current type and must not allow simultaneous conduction of forward and reverse bridges. With sufficient link reactors, cycloconverters can also operate in circulating current mode to eliminate "dead zones" between two bridges' conduction and to improve output voltage characteristics.

The output frequency of cycloconverter drives is limited to less than one-third of input line frequency, or in the case of Source converter Load converter converter circulating current type, to less than one-half of input line frequency. Cycloconverters require many switching devices **Figure 8.** Typical LCI synchronous motor drive system. with three-phase drives normally using at least 36 SCRs.

Figure 9. Typical waveforms of a LCI load converter.

sponse, and full regenerative capability. Cycloconverter deal with possible dc saturation. drives are suitable for both induction and synchronous motor applications and have been used in metal rolling mill main
drives, cement mill, ball mill, ship propulsion, variable fre-
quency generators, locomotive traction, mine winding, kilns, In a typical CSI drive, as shown in Fig quency generators, locomotive traction, mine winding, kilns,

bridges and therefore always draw lagging reactive power uses SCRs to produce the required variable output frequency
from the input. The input power factor worsens as the output by forced commutation. The six capacitors an from the input. The input power factor worsens as the output by forced commutation. The six capacitors and six diodes help
voltage or the load power factor decreases. The maximum achieve the forced commutation. The drive h voltage or the load power factor decreases. The maximum power factor for a cycloconverter is below 0.5 (12). To improve reactor to regulate the dc link current, making it appear to its power factor, a cycloconverter is operated in the trapezoi- the inverter as a current source. It is usually packaged as a dal mode rather than the standard sinusoidal mode. In the motor drive unit because proper inverter commutation detrapezoidal mode, a third harmonic component is added to the pends on motor inductance. CSIs are usually for induction fundamental phase voltage reference so that the output volt- motor drives. Figure 13 shows the voltage and current waveage is higher for each bridge and this leads to a higher power forms of one phase. The current is square wave-shaped which factor. The added third harmonic component does not affect can cause torque oscillation at low speed (cogging or torque normal operation of a neutral floating Y-connected motor pulsation) and harmonic heating in the motor. The voltage where the third harmonic current cannot flow. has notching caused by commutation.

harmonic spectrum including a dc component due to normal SCR switching and commutation. Motor voltage is controlled

Therefore, they are usually limited to low-speed, high-power dc bridge operation, cycloconverter operation directly sensed applications. They have a simple, rugged converter structure, by the source, and their interaction (12). Line-side filters are high efficiency, excellent controllability, good dynamic re- usually needed and also transformers need to be designed to

crushers, and large fans and pumps (3,7,9,10). source converter produces a variable dc voltage by firing the
Cycloconverters are made up of phase-controlled DC SCRs to maintain a proper V/Hz ratio. The inverter section Cycloconverters are made up of phase-controlled DC SCRs to maintain a proper V/Hz ratio. The inverter section
idges and therefore always draw lagging reactive nower uses SCRs to produce the required variable output frequen

Cycloconverters can have a very rich source-side current The frequency of a CSI is controlled by controlling inverter

Figure 10. Configuration of cycloconverter drives. **VSI Drives (1,2,3,7,10,11,13)**

SCR front end. They cannot work at no load and have a stabil-
time during a period, and a total of three switches are on at
ity problem at light-load and high-speed conditions. Other
disadvantages include the use of large

Figure 12. Basic configuration of CSI drives.

There are many CSI variations on Fig. 12, notably IGBTor GTO-based CSIs and PWM-CSIs (26).

A VSI drive uses the same SCR-controlled source converter by regulating dc link current. In general, voltage and current as a CSI drive. As shown in Fig. 14, its dc link and inverter feedback control are required in a CSI VSD application.
CSI drives are robust and reliable becaus CSI drives are robust and reliable because of their inher-
ent current-limiting operation. They have simple circuitry,
good dynamic behavior, inherent regenerating capability, and
are easy to control. However, CSIs have la

VSI drives usually have a speed ratio limited to $10:1$ (3). They are general purpose industrial drives, normally used in low- to medium-horsepower industrial applications (several HP to several hundred HP).

VSIs have been superseded in performance and are rarely applied nowadays (10). They are being replaced by PWM-VSI drives.

Figure 11. Operation principle of a cycloconverter (3). **Figure 13.** Motor voltage and current waveforms of CSI drives.

Figure 14. Basic configuration of VSI drives.

A typical PWM-VSI drive, shown in Fig. 16, uses a diode monics because the load current for the source bridge is
bridge for a source converter to produce a fixed dc bus voltage. nearly dc Properly switched PWM-VSIs produce bridge for a source converter to produce a fixed dc bus voltage. nearly dc. Properly switched PWM-VSIs produce nearly si-
The inverter uses high-frequency switching devices, usually nusoidal motor currents, causing no torq The inverter uses high-frequency switching devices, usually nusoidal motor currents, causing no torque pulsation and lit-
IGBTs, MCTs or GTOs, to generate pulse-width-modulated the harmonic heating in motors. They are easy IGBTs, MCTs or GTOs, to generate pulse-width-modulated tle harmonic heating in motors. They are easy to control, have square wave voltage outputs. Both the amplitude and fre-
excellent dynamic performance, and are compatib square wave voltage outputs. Both the amplitude and fre-
quency of the equivalent output voltage can be controlled. In-
multimotor applications (2.13). Possible application issues inquency of the equivalent output voltage can be controlled. In-
verter control signals are generated by a PWM modulator. In clude motor insulation overstress caused by high dV/dt due verter control signals are generated by a PWM modulator. In clude motor insulation overstress caused by high dV/dt due
the case of a sine-triangle modulator as shown in Fig. 17 (1), to high-frequency switching and motor h the case of a sine-triangle modulator as shown in Fig. 17 (1), to high-frequency switching and motor bearing currents three balanced sinusoidal reference voltages at desired fre-
caused by high common-mode voltages due to quency and amplitude are compared with a triangle signal at ing $(15-17)$. a selected switching frequency whose amplitude is determined by the dc link voltage. Depending on whether the sinusoidal reference voltage for a given phase is greater or less than the triangle voltage, the upper and lower switches of the phase leg turn on and off alternately. Figure 17 also shows waveforms of two phase voltages referenced to negative dc link and one line-to-line voltage. Depending on devices, PWM switching frequency ranges from 500 Hz for GTOs to 20 kHz for MOSFETs and IGBTs (15). Higher switching frequency leads to higher speed range, better dynamic performance, and less harmonics in motor voltages and currents but higher inverter losses.

Figure 15. Motor voltage and current waveforms of VSI drives (1). **Figure 17.** Waveforms of PWM-VSI drives (1).

Figure 16. Typical configuration of PWM-VSI drives.

PWM-VSI drives are a relatively new member in the ac drive family. They are gaining popularity because they have **PWM-VSI Drives (1–3,7,9–11,13,14)** many good features. They have very good input power factors with diode bridge front-end and relatively small line-side har-
A typical PWM-VSI drive, shown in Fig. 16, uses a diode monics caused by high common-mode voltages due to PWM switch-

Figure 18. The topology of a neutral point clamped PWM-VSI.

when S3 and S4 are on, and at midpoint of the dc link when level and three-level PWM-S2 and S3 are on. Therefore, NPC PWM-VSI is a three-level ules for this configuration. S2 and S3 are on. Therefore, NPC PWM-VSI is a three-level ules for this configuration.
inverter compared with the two-level PWM-VSI in Fig. 16. PWM-VSIs fed by diode bridge frontends have no regeneradc link voltage. The line-to-line voltage waveforms of NPC the input power for $\frac{1}{2}$ pWM-VSI have five levels as shown in Fig. 19 as compared monics are small. PWM-VSI have five levels, as shown in Fig. 19 as compared monics are small.
to three levels in Fig. 17, resulting in less motor hormonics PWM-VSIs range from MOSFET-based fractional horseto three levels in Fig. 17, resulting in less motor harmonics PWM-VSIs range from MOSFET-based fractional horse-
and less motor insulation stress at a given switching fre-
power drives to GTO-based 20 MW drives. They are w quency.

For medium-voltage high-power applications, the neutral PWM-VSIs can also be stacked to build higher voltage, point clamped (NPC) PWM-VSI is often used. As shown in higher power drives, as shown in Fig. 20. Each module has Fig. 18, it uses four switching devices and two clamping di- its own dc link and diode source bridge that must be isolated odes per phase, and the clamping diodes midpoint are tied to through a transformer. The inverter outputs are connected in the dc link capacitor neutral point. Phase potential is at a series to achieve higher voltages. Properly modulated switchpositive dc link when S1 and S2 are on, at a negative dc link ing produces multilevel output voltage waveforms. Both two-

inverter compared with the two-level PWM-VSI in Fig. 16. PWM-VSIs fed by diode bridge frontends have no regenera-
Although the neak voltage on a switching device in a two-
ive capability. For regenerating applications, a b Although the peak voltage on a switching device in a two- tive capability. For regenerating applications, a back-to-back
level PWM-VSI couply and the full delink voltage the peak volt. PWM-VSI topology can be used. The dc level PWM-VSI equals the full dc link voltage, the peak volt-
age on a switching device in a three-level is only one-half the lated by the PWM source bridge. With back-to-back topology. age on a switching device in a three-level is only one-half the lated by the PWM source bridge. With back-to-back topology,
de link voltage. The line-to-line voltage waveforms of NPC the input power factor can be controlle

Figure 19. Waveforms of a neutral point clamped PWM-VSI. power drives.

Figure 20. Stacking PWM-VSIs to build higher voltage and higher

Figure 21. Typical control loops of VSD.

used as system drives and general purpose stand-alone The speed feedback used in speed regulator can be directly

Ac Wound Rotor Induction Motor Drives

- A variable resistance connected to the induction motor **Torque Control** rotor winding can adjust motor speed. One way to
-
- an ac source. The rotor voltages are fed back to an ac

software platforms, of various architectures, digital or analog, to their mechanical orthogonality. As a result, dc drive torque centralized or distributed. However, they all perform the controllers consist simply of decou erarchical layers: (1) process control; (2) speed control; (3) torque control; (4) converter switching control. **Induction Motor Drive Torque Control.** Figure 5 shows that

etc.) command for VSDs based on process requirements. Pro- torque for a three-phase *p* pole induction motor can be derived cess controllers are external to VSDs. They can be interfaced with digitally controlled VSDs through programmable logic controllers (PLCs) or computer-based controller stations. The communication network can be a local area network (LAN) linked via RS422, Ethernet, or other standard industrial bus

VSD speed control is often identified as outer loop control be- and I_r , as well as δ angle must be controlled. In other words, cause in the language of control block diagrams it is a control a vector control scheme i loop outside of a torque controller as shown in Fig. 21. The of vector control uses rotor flux and corresponding magnetizinput to the speed regulator is the speed error, and the output ing current I_y , as well as the torque producing current I_x that of the speed regulator is the command to the inner torque is orthogonal to I_y . Therefor control loop. Because the torque regulator normally has much faster response than the speed regulator, the tuning of the speed regulator need only be concerned with mechanical load characteristics, such as system inertia. A first- or second-order proportional and integral regulator is usually used in VSD speed regulation. which is similar in form to Eq. (2) for dc motors.

drives. measured with a speed sensor or estimated on the basis of motor electrical measurements in sensorless VSDs (22).

rotor winding can adjust motor speed. One way to

achieve a variable resistance is by connecting the rotor

to a diode rectifier, which feeds a chopper that has a

from the speed control loop as in Fig. 21. It receives to

source through a cycloconverter (3,7). **Dc Drive Torque Control.** Equation (2) indicates that the torque for a dc motor is directly proportional to the product of **Armature current and field current. Therefore, it is very easy to control torque in a dc drive through either the armature through either the armature through either the armature of the structure of the structure of the s** Modern VSDs are complex power electronic equipment that
require sophisticated and coordinated control for their opera-
tion. Different drive controllers can be on various hardware or
software platforms of various architect

the induction motor stator current I_s consists of two current **Process Control** vectors, magnetizing current I_m and rotor current I_r , and δ is Process control generates speed (or position, torque, power, the angle between them, where $\delta = \tan^{-1}(R_r/(s\omega_s L_{\rm lr}))$. The

$$
T = \frac{3p}{2} L_{\rm m} I_{\rm m} I_{\rm r} \sin \delta \tag{10}
$$

protocols (18–21). Equation (10) states that induction motor torque is the result **Speed Control**
Speed Control is often identified as outer loop control be-
Speed control the motor torque through stator current, I_m
Speed control is often identified as outer loop control be-
and *I*_n as well a vector control scheme is needed. The actual implementation is orthogonal to I_y . Therefore, it is field-oriented control. Then,

$$
T = \frac{3p}{2} \frac{L_{\rm m}^2}{L_{\rm lr} + L_{\rm m}} I_y I_x \tag{11}
$$

(**c**)

Figure 22. Two generic types of motor field vector control schemes **Ac Drives Versus dc Drives** (22). Dc drives traditionally dominated VSD applications because

ented control in induction motor drives, as shown in Fig. 22. lies in the maintenance difficulties when $\frac{d}{dx}$ as $\frac{d}{dx}$ flux position using $\frac{d}{dx}$ their mechanical commutators. The direct method estimates the rotor flux position using flux their mechanical commutators.
observers. The more common indirect method first determines Ac drives are progressing rapidly and already have become observers. The more common indirect method first determines

$$
s = \frac{1}{\omega_{\rm e}} \frac{R_{\rm r}}{L_{\rm lr} + L_{\rm m}} \frac{I_{\rm x}}{I_{\rm y}}
$$

quadrature. Through decoupled I_x and I_y , motor torque can higher speeds, and have faster speed regulator response. be controlled.

Field-oriented control relies on knowledge about motor pa- **Ac Drive Evaluation** rameters. A properly tuned control can result in high induction motor performance comparable to that of dc motors. Comparisons of ac drives are summarized in Table 2.

Synchronous Motor Drive Torque Control. Torque control for synchronous motors also requires controlling current magnitude and phase angle. The principle of field-oriented vector control also applies to synchronous motors. The rotor position information needed in the control can be obtained by sensors, such as resolvers, absolute position encoders, and Hall sensors, or through sensorless estimation from motor terminal quantities (22).

For wound-field synchronous motors, field current control is part of torque control. The command current for a field current regulator is determined in a torque controller by the flux requirement in the motor direct axis.

Converter Switching Control

The converter switching control is usually accomplished through a modulator either digitally or by hardware analog circuitry. The input to a modulator is the reference voltage or current signal generated as output of the drive torque controller. The output of the modulator is the actual gating or firing command for switching devices. The carrier signal of the modulator depends on the drive converter, its topology, device switching frequency, dc link voltage or current, ac source voltage and frequency, etc. An example of a modulator for PWM-VSI is shown in Fig. 17.

There are a variety of modulator techniques for different drives, especially, for PWM drives. The basic requirements for a modulator include

- accuracy: discrete switching should yield continuous voltage or current as close as possible to its reference
- linearity between output and modulation index (the ratio between reference signal and carrier signal amplitude)
- minimum waveform distortion
- minimum switching losses
- prevention of commutative failure

VSD EVALUATION AND APPLICATION ISSUES

of their superior dynamic performance, low initial cost, and There are two basic approaches for implementing field-ori-
ted control in induction motor drives as shown in Fig. 22 lies in the maintenance difficulties with dc motors because of

the slip *s* based on the relationship the drive of choice in today's VSD applications. Because of advancements in power electronics and digital control, it has $s = \frac{1}{\omega_e} \frac{R_r}{L_{\text{lr}} + L_{\text{m}}} \frac{I_x}{I_y}$ become possible to build cost-effective ac drive power convert-
ers and to implement sophisticated control, making the performance of ac drives comparable to dc drives. In many cases, and then adds to the rotor speed to determine the rotor flux ac drives are superior because of the more robust ac motors. position. Both methods orient I_y to rotor flux and I_x to its For example, ac drives have higher power ratings, operate at

	Speed Range	Regeneration	Ac Line Harmonics	Motor Harmonics	Input PF^a	Power Ratings	Multimotor Applications	Power Dip Ride-Through
VSI	Medium to high	$DBOb$ possi- ble RGN^c needs extra CNVT	High	Medium	Medium	Low to medium	Possible	Possible
PWM-VSI	Very wide, down to zero	DBO possible RGN needs extra CNVT^d	Moderate	Low	Close to unity	Low to very high	Possible	Good
CSI	Very wide, down to zero	4-quadrant operation	Severe	High	Low	Medium to high	Difficult	None
LCI	Above 0.1 ω_0^e	4-quadrant operation	Severe	High	Low	Medium to very high	Difficult	None
CCV ^f	Zero to $1/3$ or $1/2$ of ω_{s}^{g}	4-quadrant operation	Severe	Low	Poor	High power	Difficult	None

Table 2. Summary Comparison of ac Drives

^a PF: power factor.

^b DBO: dynamic braking operation.

^c RGN: regeneration operation.

^d CNVT: converter.

^{*e*} ω₀: rated speed.

^f CCV: cycloconverter.

 $\frac{g}{g} \omega_s$: synchronous speed.

Drive Interface with Power Systems Theory Area Controller Systems Drive Interface with Motors

have power factors close to unity. In the case of a back-to- switching frequency, motor current harmonics are negligible. back VSI, CSI, or PWM, the drive can run at any controlled
power factor, making them var compensators for power
systems.
torinsulation. The reflected wave phenomenon due to the ca-
torinsulation. The reflected wave phenom

mitigation, harmonics levels from VSDs in their basic topolo- terminal *dV*/*dt* filters. gies may exceed the harmonics limits set by industrial standards, such as IEEE-519-1992. Possible mitigation methods **Motor Bearing Currents.** VSD switching mode operation can include (1) harmonic filters, which are usually bulky, expen- cause high common-mode voltages in motor windings. These sive, vulnerable to line resonance, and generally undesirable; (2) PWM rather than a phase-controlled front end where possible; and (3) multipulse techniques.

Multipulsing is very effective in reducing harmonics. A basic dc rectifier bridge has six switching devices, commutates six times per period and therefore is a six-pulse converter. Two six-pulse bridges combined can be a 12-pulse bridge if their device switching times are evenly displaced. Transformers with isolated and properly phase-shifted secondaries are necessary for multipulsing. Figure 23 shows an 18-pulse diode rectifier bridge with three 20° phase-shifted transformer secondaries. The current harmonics for this configuration is only a quarter of its six-pulse counterpart. **Figure 23.** Eighteen-pulse diode rectifier.

Ac Input Power Factors. VSDs with phase-controlled source **Motor Current Harmonics.** VSDs generate harmonics in moconverters usually have poor power factors, and the cyclo- tor currents and cause overheating, torque pulsation, and meconverter is the extreme case. To boost system power factors, chanical resonance. Depending on motors, derating or oversizcorrection devices, such as capacitor banks and other types of ing may be necessary. Other solutions include drive output filters, are required which add to system complexity and cost. filters (15). Certain drives have much better harmonic perfor-On the other hand, VSDs with diode bridge front ends usually mance than others. For example, with PWM-VSIs at adequate

ble connection between the inverter and the motor worsens Ac Line Harmonics. Operating in the switching mode, VSDs
are nonlinear equipment and generate undesirable current
and voltage harmonics back to the ac line (23–25). Without
the rest hat match the cable characteristic impe ters that match the cable characteristic impedance; (4) motor

shaft. The accumulated charge on the shaft can eventually lead to flashover and arcing through motor bearings (16,17). 10. D. S. Henderson, Variable speed electric drives—characteristics
Possible mitigation methods include (1) shaft grounding and applications, IEE Colloquium Ener Possible mitigation methods include (1) shaft grounding and applications, *IEE Colloquium Energy Efficient Environmen*brushes; (2) electrostatic shield between stator and rotor; (3) *tally Friendly Drive Syst.—Principles*, proper grounding schemes for motors and drives; and (4) com. UK, 19 June 1996, pp. 2/1–2/8. proper grounding schemes for motors and drives; and (4) common-mode filters. 11. W. Leonhard, Adjustable-speed ac drives, *Proc. IEEE,* **76**: 455–

Power semiconductor, digital control, and computer technolo-
gies will continue to be the technological driving forces behind
VSD development. As bigger and cheaper devices become
available, bigger and setter VSDs will be

FOUND TO, are beginning to dominate the medium-voltage, mgn-
power VSD market.
In power converter areas, PWM-VSI and its variants will
continue to dominate in ac VSDs and will be extended to a
very high power range because New topologies, such as matrix converters, could emerge. Soft
switching concepts and techniques (1) will be applied more
in VSD converter designs. More emphasis will be on friendly
interfaces to power systems and motors.
I

gle dedicated chip will be prevalent. On-line adaptive drive $\frac{C_{\text{tot}}}{C_{\text{tot}}}$ 1988, pp. 572–577.
and motor control will be a standard approach perhaps based 19 C Klessen and D Aybe and motor control will be a standard approach perhaps based
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norm for most VSD applications. Auto-tuning and self-com-
 20 S Appleton A uniform drive and proces norm for most VSD applications. Auto-tuning and self-com-
missioning will become the norm. Drive communication inter-
faces will be improved and standardized.
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mechanical resonance eliminators. Utility and motor friendly *Drives,* Oxford, UK, 8–10 Sept. 1993, pp. 641–646. drives will win in the marketplace. Overall VSD applications 22. Speed sensorless control of induction motors, in K. Rajashekara, will continue to grow in the forseeable future.

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VARIABLE SPEED GENERATION. See WIND TURBINES. **VDM.** See VIENNA DEVELOPMENT METHOD. **VECTOR CONTROL.** See MAGNETIC VARIABLES CONTROL.